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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 09/609,107 06/30/2000 PAT 6,617,226

*Loke*

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 11-187053 06/30/1999

JAPAN 11-263742 09/17/1999

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## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 01/02/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS DRAWING	TOTAL CLAIMS	INDEPENDENT CLAIMS
Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance met	JAPAN	19	5	3
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## TITLE

Semiconductor device and method for manufacturing the same

FILING FEES	FEES: Authority has been given in Paper	<input type="checkbox"/> All Fees
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